

Practitioner's Docket No. NVIDP033A/P000873

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: James M. Van Dyke et al.

Application No.: 10/657,957

Filed: September 8, 2003

For: SYSTEM AND METHOD FOR A HIGH BANDWIDTH-LOW LATENCY MEMORY
CONTROLLERMail Stop AF
Commissioner for Patents
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Alexandria, VA 22313-1450RECEIVED
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FEB 18 2005NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES
(37 C.F.R. § 1.191)

Applicant hereby appeals to the Board from the decision of the Primary Examiner, mailed November 12, 2004, finally rejecting claims 1-24, 26, and 27.

1. STATUS OF APPLICANT

This application is on behalf of other than a small entity.

01 FC:1401 500.00 DA 120.00 DA 02 FC:1251

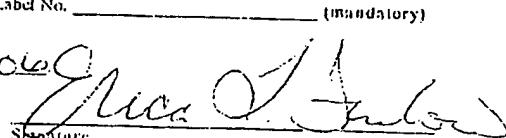
03/02/2005 FBRISCODE 00000001 501351 10657957

CERTIFICATION UNDER 37 C.F.R. "1.8(a) and 1.10*
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Date: 2/18/2005

Erica L. Farlow
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* Only the date of filing (1.6) will be the date used in a patent term adjustment calculation, although the date on any certificate of mailing or transmission under 1.8 continues to be taken into account in determining timeliness. See 1.703(f). Consider "Express Mail Post Office to Addressee" 1.10(j) or facsimile transmission 1.6(d) for the reply to be accorded the earliest possible filing date for patent term adjustment calculations.

Notice of Appeal from the Primary Examiner to Board--page 1 of 2

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